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3	276	controller same search\$3 same parallel	USPAT	2004/07/14 16:13
5	0	((707/3 707/10).ccls.) and (controller same search\$3 same parallel) and ((central priority) adj1 controller)	USPAT	2004/07/14 16:14
4	6	((707/3 707/10).ccls.) and (controller same search\$3 same parallel)	USPAT	2004/07/14 16:18
6	2797	address adj1 cache	USPAT	2004/07/14 16:19
7	0	memory and edit and search and (controller same search\$3 same parallel) and (address adj1 cache)	USPAT	2004/07/14 16:20
8	3	(edit delete add) and search\$3 and (controller same search\$3 same parallel) and (address adj1 cache)	USPAT	2004/07/14 16:21
9	406	engine adj1 level	USPAT	2004/07/14 16:22
10	1	(engine adj1 level) and ((central priority) adj1 controller) and ((707/3 707/10).ccls.)	USPAT	2004/07/14 16:24
11	4522	(central andpriority) adj1 controller\$1	USPAT	2004/07/14 16:26
12	13241	707/\$.ccls.	USPAT	2004/07/14 16:25
13	4190	search adj1 (engine module unit)	USPAT	2004/07/14 16:26
14	3	(central and priority) adj1 controller\$1	USPAT	2004/07/14 16:28
15	940	control\$6 and search\$3 and (parallel near3 execution)	USPAT	2004/07/14 16:29
16	59	(control\$6 and search\$3 and (parallel near3 execution)) and ((707/3 707/10).ccls.)	USPAT	2004/07/14 16:29
17	4165	search\$3 near5 level	USPAT	2004/07/14 16:30
18	0	((control\$6 and search\$3 and (parallel near3 execution)) and ((707/3 707/10).ccls.)) and (search\$3 near5 level) and (address adj1 cache)	USPAT	2004/07/14 16:30
19	1	(control\$6 and search\$3 and (parallel near3 execution)) and (search\$3 near5 level) and (address adj1 cache)	USPAT	2004/07/14 16:32
21	0	(FFS adj1 search) and ((707/3 707/10).ccls.)	USPAT	2004/07/14 16:32
20	14	FFS adj1 search	USPAT	2004/07/14 16:34
22	1	((control\$6 and search\$3 and (parallel near3 execution)) and ((707/3 707/10).ccls.)) and ((central priority) adj1 controller)	USPAT	2004/07/14 16:36
23	358	(search\$3 and controller).ab.	USPAT	2004/07/14 16:37
24	11	((search\$3 and controller).ab.) and ((707/3 707/10).ccls.)	USPAT	2004/07/14 16:43
25	233	priority adj1 controller	USPAT	2004/07/14 17:01
26	4522	central adj1 controller	USPAT	2004/07/14 17:02
27	195	binary adj1 search adj1 tree	USPAT	2004/07/14 17:03
28	214	IPv6 near5 protocol\$1	USPAT	2004/07/14 17:03
29	6351	(simultaneous parallel) near9 (edit delete add modification)	USPAT	2004/07/14 17:04
30	1314990	level\$1	USPAT	2004/07/14 17:04
31	0	(priority adj1 controller) and (central adj1 controller) and (binary adj1 search adj1 tree)	USPAT	2004/07/14 17:05
32	2	((priority adj1 controller) (central adj1 controller)) and (binary adj1 search adj1 tree)	USPAT	2004/07/14 17:06
33	6	(IPv6 near5 protocol\$1) and ((707/3 707/10).ccls.)	USPAT	2004/07/14 17:10

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Networking, IEEE/ACM Transactions on , Volume: 6 , Issue: 5 , Oct. 1998

Pages:544 - 557

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Pages:163 - 168[\[Abstract\]](#) [\[PDF Full-Text \(533 KB\)\]](#) **IEEE JNL****2 PIN-a PC-based robot simulation and offline programming system using macro programming techniques***Wenrui Dai; Kampker, M.;*Industrial Electronics Society, 1999. IECON '99 Proceedings. The 25th Annual Conference of the IEEE , Volume: 1 , 29 Nov.-3 Dec. 1999
Pages:442 - 446 vol.1[\[Abstract\]](#) [\[PDF Full-Text \(796 KB\)\]](#) **IEEE CNF****3 Probability based replacement algorithm for WWW server arrays***Yeung, K.H.; Suen, K.W.;*Parallel and Distributed Systems, 1998. Proceedings., 1998 International Conference on , 14-16 Dec. 1998
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Info-tech and Info-net, 2001. Proceedings. ICII 2001 - Beijing. 2001 International Conferences on , Volume: 4 , 29 Oct.-1 Nov. 2001

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1 A CMOS feedforward neural-network chip with on-chip parallel learning for oscillation cancellation*Liu, J.; Brooke, M.A.; Hirotsu, K.;*Neural Networks, IEEE Transactions on , Volume: 13 , Issue: 5 , Sep 2002
Pages:1178 - 1186[\[Abstract\]](#) [\[PDF Full-Text \(457 KB\)\]](#) IEEE JNL**2 Image processing LSI "ISP-IV" based on local parallel architecture and its applications***Muramatsu, S.; Kobayashi, Y.; Araoka, M.; Naoi, S.; Kaneta, T.; Hirose, K.; Onizawa, S.;*Image Processing, 1998. ICIP 98. Proceedings. 1998 International Conference on , 4-7 Oct. 1998
Pages:1000 - 1004 vol.3[\[Abstract\]](#) [\[PDF Full-Text \(651 KB\)\]](#) IEEE CNF

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Tables of Contents

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- ☐ Conference Proceedings
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- ☐ Advanced

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1 Parallel search in matrices with sorted columns

Jain, A.;

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3 Distributed game-tree search using transposition table driven work scheduling

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4 Load balancing strategies for parallel forward search algorithm with conflict based backjumping

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7 Optimal and near-optimal allocation of precedence-constrained task to parallel processors: defying the high complexity using effective search techniques

Ahmad, I.; Yu-Kwong Kwok;

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8 Parallel bidirectional heuristic search with dynamic process re-direction

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14 **Unsynchronized iteratively deepening parallel alpha-beta search**
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